

# INTERNATIONAL STANDARD

# NORME INTERNATIONALE

**Semiconductor devices – Non-destructive recognition criteria of defects in silicon carbide homoepitaxial wafer for power devices –  
Part 3: Test method for defects using photoluminescence**

**Dispositifs à semiconducteurs – Critères de reconnaissance non destructifs des défauts au sein d'une plaquette homoépitaxiale de carbone de silicium pour des dispositifs d'alimentation –  
Partie 3: Méthode d'essai pour les défauts à l'aide de la photoluminescence**



## THIS PUBLICATION IS COPYRIGHT PROTECTED

Copyright © 2020 IEC, Geneva, Switzerland

All rights reserved. Unless otherwise specified, no part of this publication may be reproduced or utilized in any form or by any means, electronic or mechanical, including photocopying and microfilm, without permission in writing from either IEC or IEC's member National Committee in the country of the requester. If you have any questions about IEC copyright or have an enquiry about obtaining additional rights to this publication, please contact the address below or your local IEC member National Committee for further information.

Droits de reproduction réservés. Sauf indication contraire, aucune partie de cette publication ne peut être reproduite ni utilisée sous quelque forme que ce soit et par aucun procédé, électronique ou mécanique, y compris la photocopie et les microfilms, sans l'accord écrit de l'IEC ou du Comité national de l'IEC du pays du demandeur. Si vous avez des questions sur le copyright de l'IEC ou si vous désirez obtenir des droits supplémentaires sur cette publication, utilisez les coordonnées ci-après ou contactez le Comité national de l'IEC de votre pays de résidence.

IEC Central Office  
3, rue de Varembé  
CH-1211 Geneva 20  
Switzerland

Tel.: +41 22 919 02 11  
[info@iec.ch](mailto:info@iec.ch)  
[www.iec.ch](http://www.iec.ch)

### About the IEC

The International Electrotechnical Commission (IEC) is the leading global organization that prepares and publishes International Standards for all electrical, electronic and related technologies.

### About IEC publications

The technical content of IEC publications is kept under constant review by the IEC. Please make sure that you have the latest edition, a corrigendum or an amendment might have been published.

#### IEC publications search - [webstore.iec.ch/advsearchform](http://webstore.iec.ch/advsearchform)

The advanced search enables to find IEC publications by a variety of criteria (reference number, text, technical committee,...). It also gives information on projects, replaced and withdrawn publications.

#### IEC Just Published - [webstore.iec.ch/justpublished](http://webstore.iec.ch/justpublished)

Stay up to date on all new IEC publications. Just Published details all new publications released. Available online and once a month by email.

#### IEC Customer Service Centre - [webstore.iec.ch/csc](http://webstore.iec.ch/csc)

If you wish to give us your feedback on this publication or need further assistance, please contact the Customer Service Centre: [sales@iec.ch](mailto:sales@iec.ch).

#### Electropedia - [www.electropedia.org](http://www.electropedia.org)

The world's leading online dictionary on electrotechnology, containing more than 22 000 terminological entries in English and French, with equivalent terms in 16 additional languages. Also known as the International Electrotechnical Vocabulary (IEV) online.

#### IEC Glossary - [std.iec.ch/glossary](http://std.iec.ch/glossary)

67 000 electrotechnical terminology entries in English and French extracted from the Terms and Definitions clause of IEC publications issued since 2002. Some entries have been collected from earlier publications of IEC TC 37, 77, 86 and CISPR.

### A propos de l'IEC

La Commission Electrotechnique Internationale (IEC) est la première organisation mondiale qui élabore et publie des Normes internationales pour tout ce qui a trait à l'électricité, à l'électronique et aux technologies apparentées.

### A propos des publications IEC

Le contenu technique des publications IEC est constamment revu. Veuillez vous assurer que vous possédez l'édition la plus récente, un corrigendum ou amendement peut avoir été publié.

#### Recherche de publications IEC - [webstore.iec.ch/advsearchform](http://webstore.iec.ch/advsearchform)

La recherche avancée permet de trouver des publications IEC en utilisant différents critères (numéro de référence, texte, comité d'études,...). Elle donne aussi des informations sur les projets et les publications remplacées ou retirées.

#### IEC Just Published - [webstore.iec.ch/justpublished](http://webstore.iec.ch/justpublished)

Restez informé sur les nouvelles publications IEC. Just Published détaille les nouvelles publications parues. Disponible en ligne et une fois par mois par email.

#### Service Clients - [webstore.iec.ch/csc](http://webstore.iec.ch/csc)

Si vous désirez nous donner des commentaires sur cette publication ou si vous avez des questions contactez-nous: [sales@iec.ch](mailto:sales@iec.ch).

#### Electropedia - [www.electropedia.org](http://www.electropedia.org)

Le premier dictionnaire d'électrotechnologie en ligne au monde, avec plus de 22 000 articles terminologiques en anglais et en français, ainsi que les termes équivalents dans 16 langues additionnelles. Egalement appelé Vocabulaire Electrotechnique International (IEV) en ligne.

#### Glossaire IEC - [std.iec.ch/glossary](http://std.iec.ch/glossary)

67 000 entrées terminologiques électrotechniques, en anglais et en français, extraites des articles Termes et Définitions des publications IEC parues depuis 2002. Plus certaines entrées antérieures extraites des publications des CE 37, 77, 86 et CISPR de l'IEC.



IEC 63068-3

Edition 1.0 2020-07

# INTERNATIONAL STANDARD

# NORME INTERNATIONALE

**Semiconductor devices – Non-destructive recognition criteria of defects in silicon carbide homoepitaxial wafer for power devices –  
Part 3: Test method for defects using photoluminescence**

**Dispositifs à semiconducteurs – Critères de reconnaissance non destructifs des défauts au sein d'une plaquette homoépitaxiale de carbure de silicium pour des dispositifs d'alimentation –  
Partie 3: Méthode d'essai pour les défauts à l'aide de la photoluminescence**

INTERNATIONAL  
ELECTROTECHNICAL  
COMMISSION

COMMISSION  
ELECTROTECHNIQUE  
INTERNATIONALE

ICS 31.080.99

ISBN 978-2-8322-8614-2

**Warning! Make sure that you obtained this publication from an authorized distributor.  
Attention! Veuillez vous assurer que vous avez obtenu cette publication via un distributeur agréé.**

## CONTENTS

|  |    |
|--|----|
| FOREWORD .....   | 4  |
| INTRODUCTION .....   | 6  |
| 1 Scope .....  | 7  |
| 2 Normative references .....                                     | 7  |
| 3 Terms and definitions .....                                    | 7  |
| 4 Photoluminescence method .....                                 | 11 |
| 4.1 General .....  | 11 |
| 4.2 Principle .....  | 11 |
| 4.3 Requirements .....   | 11 |
| 4.3.1 Measuring equipment .....                                  | 11 |
| 4.3.2 Wafer positioning and focusing .....                       | 13 |
| 4.3.3 Image capturing .....                                      | 13 |
| 4.3.4 Image processing .....                                     | 13 |
| 4.3.5 Image analysis .....                                       | 13 |
| 4.3.6 Image evaluation .....                                     | 14 |
| 4.3.7 Documentation .....  | 14 |
| 4.4 Parameter settings .....                                     | 14 |
| 4.4.1 General .....  | 14 |
| 4.4.2 Parameter setting process .....                            | 14 |
| 4.5 Procedure .....  | 14 |
| 4.6 Evaluation .....   | 14 |
| 4.6.1 General .....  | 14 |
| 4.6.2 Mean width of planar and volume defects .....              | 14 |
| 4.6.3 Evaluation process .....                                   | 15 |
| 4.7 Precision .....  | 15 |
| 4.8 Test report .....  | 15 |
| 4.8.1 Mandatory elements .....                                   | 15 |
| 4.8.2 Optional elements .....                                    | 15 |
| Annex A (informative) Photoluminescence images of defects .....  | 16 |
| A.1 General .....  | 16 |
| A.2 BPD .....  | 16 |
| A.3 Stacking fault .....   | 17 |
| A.4 Propagated stacking fault .....                              | 18 |
| A.5 Stacking fault complex .....                                 | 19 |
| A.6 Polytype inclusion .....                                     | 19 |
| Annex B (informative) Photoluminescence spectra of defects ..... | 21 |
| B.1 General .....  | 21 |
| B.2 BPD .....  | 21 |
| B.3 Stacking fault .....   | 21 |
| B.4 Propagated stacking fault .....                              | 23 |
| B.5 Stacking fault complex .....                                 | 23 |
| B.6 Polytype inclusion .....                                     | 24 |
| Bibliography .....   | 25 |
| Figure 1 – Schematic diagram of PL imaging system .....          | 12 |

|  |    |
|--|----|
| Figure A.1 – BPD .....   | 17 |
| Figure A.2 – Stacking fault.....   | 18 |
| Figure A.3 – Propagated stacking fault .....   | 18 |
| Figure A.4 – Stacking fault complex .....  | 19 |
| Figure A.5 – Polytype inclusion .....  | 20 |
| Figure B.1 – PL spectrum from BPD .....  | 21 |
| Figure B.2 – PL spectra from Frank-type stacking faults .....  | 22 |
| Figure B.3 – PL spectra from Shockley-type stacking faults .....                                     | 22 |
| Figure B.4 – PL spectra from various stacking faults in the wavelength range longer than 650 nm..... | 23 |
| Figure B.5 – PL spectrum from stacking fault complex .....   | 24 |
| Figure B.6 – PL spectrum from polytype inclusion .....   | 24 |

## INTERNATIONAL ELECTROTECHNICAL COMMISSION

**SEMICONDUCTOR DEVICES –  
NON-DESTRUCTIVE RECOGNITION CRITERIA OF DEFECTS  
IN SILICON CARBIDE HOMOEPIТАXIAL WAFER FOR POWER DEVICES –**

**Part 3: Test method for defects using photoluminescence**

**FOREWORD**

- 1) The International Electrotechnical Commission (IEC) is a worldwide organization for standardization comprising all national electrotechnical committees (IEC National Committees). The object of IEC is to promote international co-operation on all questions concerning standardization in the electrical and electronic fields. To this end and in addition to other activities, IEC publishes International Standards, Technical Specifications, Technical Reports, Publicly Available Specifications (PAS) and Guides (hereafter referred to as "IEC Publication(s)"). Their preparation is entrusted to technical committees; any IEC National Committee interested in the subject dealt with may participate in this preparatory work. International, governmental and non-governmental organizations liaising with the IEC also participate in this preparation. IEC collaborates closely with the International Organization for Standardization (ISO) in accordance with conditions determined by agreement between the two organizations.
- 2) The formal decisions or agreements of IEC on technical matters express, as nearly as possible, an international consensus of opinion on the relevant subjects since each technical committee has representation from all interested IEC National Committees.
- 3) IEC Publications have the form of recommendations for international use and are accepted by IEC National Committees in that sense. While all reasonable efforts are made to ensure that the technical content of IEC Publications is accurate, IEC cannot be held responsible for the way in which they are used or for any misinterpretation by any end user.
- 4) In order to promote international uniformity, IEC National Committees undertake to apply IEC Publications transparently to the maximum extent possible in their national and regional publications. Any divergence between any IEC Publication and the corresponding national or regional publication shall be clearly indicated in the latter.
- 5) IEC itself does not provide any attestation of conformity. Independent certification bodies provide conformity assessment services and, in some areas, access to IEC marks of conformity. IEC is not responsible for any services carried out by independent certification bodies.
- 6) All users should ensure that they have the latest edition of this publication.
- 7) No liability shall attach to IEC or its directors, employees, servants or agents including individual experts and members of its technical committees and IEC National Committees for any personal injury, property damage or other damage of any nature whatsoever, whether direct or indirect, or for costs (including legal fees) and expenses arising out of the publication, use of, or reliance upon, this IEC Publication or any other IEC Publications.
- 8) Attention is drawn to the Normative references cited in this publication. Use of the referenced publications is indispensable for the correct application of this publication.
- 9) Attention is drawn to the possibility that some of the elements of this IEC Publication may be the subject of patent rights. IEC shall not be held responsible for identifying any or all such patent rights.

International Standard IEC 63068-3 has been prepared by IEC technical committee 47: Semiconductor devices.

The text of this International Standard is based on the following documents:

| FDIS         | Report on voting |
|--------------|------------------|
| 47/2628/FDIS | 47/2638/RVD      |

Full information on the voting for the approval of this International Standard can be found in the report on voting indicated in the above table.

This document has been drafted in accordance with the ISO/IEC Directives, Part 2.

A list of all parts in the IEC 63068 series, published under the general title *Semiconductor devices – Non-destructive recognition criteria of defects in silicon carbide homoepitaxial wafer for power devices*, can be found on the IEC website.

The committee has decided that the contents of this document will remain unchanged until the stability date indicated on the IEC website under "<http://webstore.iec.ch>" in the data related to the specific document. At this date, the document will be

- reconfirmed,
- withdrawn,
- replaced by a revised edition, or
- amended.

## INTRODUCTION

Silicon carbide (SiC) is widely used as a semiconductor material for next-generation power semiconductor devices. SiC, as compared with silicon (Si), has superior physical properties such as a higher breakdown electric field, higher thermal conductivity, lower thermal generation rate, higher saturated electron drift velocity, and lower intrinsic carrier concentration. These attributes realize SiC-based power semiconductor devices with faster switching speeds, lower losses, higher blocking voltages, and higher temperature operation relative to standard Si-based power semiconductor devices.

SiC-based power semiconductor devices are not fully realized due to some issues including high costs, low yield, and low long-term reliability. In particular, one of the serious issues lies in the defects existing in SiC homoepitaxial wafers. Although efforts of decreasing defects in SiC homoepitaxial wafers are actively implemented, there are a number of defects in commercially available SiC homoepitaxial wafers. Therefore, it is indispensable to establish an international standard regarding the quality assessment of SiC homoepitaxial wafers.

The IEC 63068 series of standards is planned to comprise Part 1, Part 2, and Part 3, as detailed below. This document provides definitions and guidance in use of photoluminescence for detecting defects in commercially available silicon carbide (SiC) homoepitaxial wafers.

Part 1: Classification of defects

Part 2: Test method for defects using optical inspection

Part 3: Test method for defects using photoluminescence

**SEMICONDUCTOR DEVICES –  
NON-DESTRUCTIVE RECOGNITION CRITERIA OF DEFECTS  
IN SILICON CARBIDE HOMOEPIXTAL WAFER FOR POWER DEVICES –**

**Part 3: Test method for defects using photoluminescence**

## **1 Scope**

This part of IEC 63068 provides definitions and guidance in use of photoluminescence for detecting as-grown defects in commercially available 4H-SiC (Silicon Carbide) epitaxial wafers. Additionally, this document exemplifies photoluminescence images and emission spectra to enable the detection and categorization of the defects in SiC homoepitaxial wafers.

## **2 Normative references**

There are no normative references in this document.

## **3 Terms and definitions**

For the purposes of this document, the following terms and definitions apply.

ISO and IEC maintain terminological databases for use in standardization at the following addresses:

- IEC Electropedia: available at <http://www.electropedia.org/>
- ISO Online browsing platform: available at <http://www.iso.org/obp>

### **3.1**

#### **photoluminescence**

##### **PL**

emission of light from materials as a subsequence of electronic excitation by absorption of photons

### **3.2**

#### **photoluminescence imaging**

##### **PL imaging**

technique for capturing, processing and analysing images of defects using light source for electronic excitation, focusing optics, optical filter, optical image sensor and computer systems

### **3.3**

#### **focusing optics**

lens system used for magnifying and capturing optical images

### **3.4**

#### **optical filter**

optical component designed to transmit only a specific wavelength region and to block other regions

### **3.5**

#### **optical image sensor**

device to transform an optical image into digital data