

Circuit boards and circuit board assemblies - Design and use - Part 6-2: Land pattern design - Description of land pattern for the most common surface mounted components (SMD)

EESTI STANDARDI EESSÕNA

NATIONAL FOREWORD

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English Version

Circuit boards and circuit board assemblies - Design and use -
Part 6-2: Land pattern design - Description of land pattern for the
most common surface mounted components (SMD)
(IEC 61188-6-2:2021)

Cartes imprimées et cartes imprimées équipées -
Conception et utilisation - Partie 6-2: Conception de la zone
de report - Description de la zone de report pour les
composants montés en surface (CMS) les plus courants
(IEC 61188-6-2:2021)

Leiterplatten und Flachbaugruppen - Konstruktion und
Anwendung - Teil 6-2: Anschlussflächenbild - Beschreibung
des Anschlussflächenbilds für die meisten
oberflächenmontierbaren Bauelemente (SMD, en: surface
mounted components)
(IEC 61188-6-2:2021)

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European foreword

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- latest date by which the document has to be implemented at national level by publication of an identical national standard or by endorsement (dop) 2021-12-11
- latest date by which the national standards conflicting with the document have to be withdrawn (dow) 2024-03-11

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In the official version, for Bibliography, the following notes have to be added for the standards indicated:

IEC 61188-5-1:2002	NOTE	Harmonized as EN 61188-5-1:2002 (not modified)
IEC 61188-5-2	NOTE	Harmonized as EN 61188-5-2
IEC 61188-5-3	NOTE	Harmonized as EN 61188-5-3
IEC 61188-5-4	NOTE	Harmonized as EN 61188-5-4
IEC 61188-5-5	NOTE	Harmonized as EN 61188-5-5
IEC 61188-5-6	NOTE	Harmonized as EN 61188-5-6
IEC 61188-5-8	NOTE	Harmonized as EN 61188-5-8
IEC 61191 (series)	NOTE	Harmonized as EN 61191 (series)

Annex ZA (normative)

Normative references to international publications with their corresponding European publications

The following documents are referred to in the text in such a way that some or all of their content constitutes requirements of this document. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

NOTE 1 Where an International Publication has been modified by common modifications, indicated by (mod), the relevant EN/HD applies.

NOTE 2 Up-to-date information on the latest versions of the European Standards listed in this annex is available here: www.cenelec.eu.

<u>Publication</u>	<u>Year</u>	<u>Title</u>	<u>EN/HD</u>	<u>Year</u>
IEC 60194-2	-	Printed boards design, manufacture and assembly - Vocabulary - Part 2: Common usage in electronic technologies as well as printed board and electronic assembly technologies	-	-
IEC 61188-6-1	-	Circuit boards and circuit board assemblies - Design and use - Part 6-1: Land pattern design - Generic requirements for land pattern on circuit boards	EN IEC 61188-6-1 ¹	-
IEC 61188-6-4	-	Printed boards and printed board assemblies - Design and use - Part 6-4: Land pattern design - Generic requirements for dimensional drawings of surface mounted components (SMD) from the viewpoint of land pattern design	EN IEC 61188-6-4	-
IEC 61191-2	2017	Printed board assemblies - Part 2: Sectional specification - Requirements for surface mount soldered assemblies	EN 61191-2	2017

¹ To be published. Stage at the time of publication: prEN IEC 61188-6-1:2020.

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surface mounted components (SMD)**

**Cartes imprimées et cartes imprimées équipées – Conception et utilisation –
Partie 6-2: Conception de la zone de report – Description de la zone de report
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**CIRCUIT BOARDS AND CIRCUIT BOARD ASSEMBLIES –
DESIGN AND USE –****Part 6-2: Land pattern design – Description of land pattern
for the most common surface mounted components (SMD)**

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The language used for the development of this International Standard is English.

This document was drafted in accordance with ISO/IEC Directives, Part 2, and developed in accordance with ISO/IEC Directives, Part 1 and ISO/IEC Directives, IEC Supplement, available at www.iec.ch/members_experts/refdocs. The main document types developed by IEC are described in greater detail at www.iec.ch/standardsdev/publications.

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