

ISO/IEC TS 11801-9903

Edition 1.0 2021-03

TECHNICAL SPECIFICATION



Information technology – Generic cabling systems for customer premises – Part 9903: Matrix modelling of channels and links



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g sy, s and lin. Information technology – Generic cabling systems for customer premises – Part 9903: Matrix modelling of channels and links

INTERNATIONAL ELECTROTECHNICAL COMMISSION

ISBN 978-2-8322-9498-7

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ICS 35.200

CONTENTS

INTRODUCTION 7 1 Scope 9 2 Normative references 9 3 Terms, definitions and abbreviated terms 9 3.1 Terms and definitions 9 3.2 Symbols and abbreviated terms 10 4 Matrix model 11 5.1 General 11 5.2 Quadriports 11 5.3 Matrix port definition for a two-pair system representative for modelling purposes 11 5.4 Operational scattering matrix 12 5.5 General naming convention 12 5.6 S-matrix 13 5.7 Passivity 13 5.8 Operational reflection loss matrix 14 5.9 Transmission matrix (T-matrix) 14 5.10 S-matrix of cabling 14 6 Calculation with matrices using limit lines 15 7 Extracting limit lines 15 8 General 16 8.1 General 16 8.2 M-parameters 17	FC	FOREWORD			
2 Normative references 9 3 Terms, definitions and abbreviated terms 9 3.1 Terms and definitions. 9 3.2 Symbols and abbreviated terms 10 4 Matrix model 11 5 Matrix definition 11 5.1 General 11 5.2 Quadriports 11 5.3 Matrix port definition for a two-pair system representative for modelling purposes 11 5.4 Operational scattering matrix 12 5.5 General naming convention 12 5.6 S-matrix 13 5.7 Passivity 13 5.8 Operational reflection loss matrix 14 5.9 Transmission matrix (T-matrix) 14 5.10 S-matrix of cabling 14 6 Calculation with matrices using limit lines 15 7 Extracting limit lines 15 8 General 16 8.1 General 17 9.2 <td>IN'</td> <td>TRODU</td> <td>CTION</td> <td>.7</td>	IN'	TRODU	CTION	.7	
3 Terms, definitions and abbreviated terms 9 3.1 Terms and definitions. 9 3.2 Symbols and abbreviated terms 10 4 Matrix model. 11 5 Matrix definition 11 5.1 General. 11 5.1 General. 11 5.2 Quadriports. 11 5.3 Matrix port definition for a two-pair system representative for modelling purposes. 11 5.4 Operational scattering matrix 12 5.5 General naming convention. 12 5.6 S-matrix 13 5.7 Passivity 13 5.8 Operational reflection loss matrix 14 5.10 S-matrix of cabling 14 5.10 S-matrix of cabling 14 6 Calculation with matrices using limit lines 15 7 Extracting limit lines 16 8.1 General 17 9.1 General 17 9.2 Equations to extract the cabling limit lines 17 9.2.1	1	Scop	e	.9	
3.1 Terms and definitions	2	Norm	ative references	.9	
3.2 Symbols and abbreviated terms 10 4 Matrix model 11 5 Matrix definition 11 5.1 General 11 5.2 Quadriports 11 5.3 Matrix port definition for a two-pair system representative for modelling purposes 11 5.3 Matrix port definition for a two-pair system representative for modelling purposes 12 5.6 General naming convention 12 5.6 S-matrix 13 5.7 Passivity 13 5.8 Operational reflection loss matrix 14 5.9 Transmission matrix (T-matrix) 14 5.10 S-matrix of cabing 14 6 Calculation with matrices using limit lines 15 7 Extracting limit lines 15 8 General case using mixed-mode matrices 16 8.1 General 16 8.2 M-parameters 16 9 Submatrix DD 17 9.1 General 17 9.2.2 Operational attenuation 17	3	Term	s, definitions and abbreviated terms	.9	
3.2 Symbols and abbreviated terms 10 4 Matrix model 11 5 Matrix definition 11 5.1 General 11 5.2 Quadriports 11 5.3 Matrix port definition for a two-pair system representative for modelling purposes 11 5.3 Matrix port definition for a two-pair system representative for modelling purposes 12 5.6 General naming convention 12 5.6 S-matrix 13 5.7 Passivity 13 5.8 Operational reflection loss matrix 14 5.9 Transmission matrix (T-matrix) 14 5.10 S-matrix of cabing 14 6 Calculation with matrices using limit lines 15 7 Extracting limit lines 15 8 General case using mixed-mode matrices 16 8.1 General 16 8.2 M-parameters 16 9 Submatrix DD 17 9.1 General 17 9.2.2 Operational attenuation 17		3.1	Terms and definitions	.9	
4 Matrix model 11 5 Matrix definition 11 5.1 General 11 5.2 Quadriports 11 5.3 Matrix port definition for a two-pair system representative for modelling purposes 11 5.4 Operational scattering matrix 12 5.5 General naming convention 12 5.6 S-matrix 13 5.7 Passivity 13 5.8 Operational reflection loss matrix 14 5.9 Transmission matrix (T-matrix) 14 5.10 S-matrix of cabling 14 6 Calculation with matrices using limit lines 15 7 Extracting limit lines 15 8 General 16 8.1 General 16 8.2 M-parameters 16 8 Submatrix DD 17 9.1 General 17 9.2.1 General 17 9.2.2 Operational attenuation 17 9.2.3 Near-end crosstalk ratio 17		•••			
5 Matrix definition 11 5.1 General 11 5.2 Quadriports 11 5.3 Matrix port definition for a two-pair system representative for modelling purposes 11 5.4 Operational scattering matrix 12 5.5 General naming convention 12 5.6 S-matrix 13 5.7 Passivity 13 5.8 Operational reflection loss matrix 14 5.9 Transmission matrix (T-matrix) 14 5.10 S-matrix of cabling 14 6 Calculation with matrices using limit lines 15 7 Extracting limit lines 15 8 General case using mixed-mode matrices 16 8.1 General 16 8.2 M-parameters 16 9 Submatrix DD 17 9.2.1 General 17 9.2.2 Operational attenuation 17 9.2.3 Near-end crosstalk ratio 17 9.2.4 Attenuation to far-end crosstalk ratio 17 9.2.3 </td <td>4</td> <td>Matri</td> <td></td> <td></td>	4	Matri			
5.1 General 11 5.2 Quadriports 11 5.3 Matrix port definition for a two-pair system representative for modelling purposes 11 5.4 Operational scattering matrix 12 5.5 General naming convention 12 5.6 S-matrix 13 5.7 Passivity 13 5.8 Operational reflection loss matrix 14 5.9 Transmission matrix (T-matrix) 14 5.10 S-matrix of cabling 14 6 Calculation with matrices using limit lines 15 7 Extracting limit lines 15 8 General case using mixed-mode matrices 16 8.1 General 16 8.2 M-parameters 16 8.2 M-parameters 16 8.2 M-parameters 16 9 Submatrix DD 17 9.1 General 17 9.2.2 Operational attenuation 17 9.2.3 Near-end crosstalk ratio 17 9.2.4 Attenuation to far-	5				
5.2 Quadriports 11 5.3 Matrix port definition for a two-pair system representative for modelling purposes 11 5.4 Operational scattering matrix 11 5.5 General naming convention 12 5.6 S-matrix 13 5.7 Passivity 13 5.8 Operational reflection loss matrix 14 5.9 Transmission matrix (T-matrix) 14 5.10 S-matrix of cabling 14 6 Calculation with matrices using limit lines 15 7 Extracting limit lines 15 8 General case using mixed-mode matrices 16 8.1 General 16 8.2 M-parameters 16 9 Submatrix DD 17 9.1 General 17 9.2.2 Operational attenuation 17 9.2.3 Near-end crosstalk 17 9.2.4 Attenuation to far-end crosstalk ratio 17 9.2.5 Reflection (RL) 18 10 Component values to be used as input to the model 18	-				
5.3 Matrix port definition for a two-pair system representative for modelling purposes. 11 5.4 Operational scattering matrix 12 5.5 General naming convention 12 5.6 S-matrix 13 5.7 Passivity 13 5.8 Operational reflection loss matrix 14 5.9 Transmission matrix (T-matrix) 14 5.10 S-matrix of cabling 14 6 Calculation with matrices using limit lines 15 7 Extracting limit lines 15 8 General case using mixed-mode matrices 16 8.1 General 16 8.2 M-parameters 16 9 Submatrix DD 17 9.1 General 17 9.2 Operational attenuation 17 9.2.1 General 17 9.2.2 Operational attenuation 17 9.2.3 Reflection (RL) 17 9.2.4 Attenuation to far-end crosstalk ratio 17 9.2.5 Reflection (RL) 18 1		-			
purposes 11 5.4 Operational scattering matrix 12 5.5 General naming convention 12 5.6 S-matrix 13 5.7 Passivity 13 5.7 Passivity 13 5.7 Passivity 13 5.8 Operational reflection loss matrix 14 5.9 Transmission matrix (T-matrix) 14 5.0 S-matrix of cabling 14 6 Calculation with matrices using limit lines 15 7 Extracting limit lines 15 8 General case using mixed-mode matrices 16 8.1 General 16 8.2 M-parameters 16 9 Submatrix DD 17 9.1 General 17 9.2 Equations to extract the cabling limit lines 17 9.2.1 General 17 9.2.2 Operational attenuation 17 9.2.3 Near-end crosstalk 17		-		• •	
5.5 General naming convention 12 5.6 S-matrix 13 5.7 Passivity 13 5.8 Operational reflection loss matrix 14 5.9 Transmission matrix (T-matrix) 14 5.0 S-matrix of cabling 14 6 Calculation with matrices using limit lines 15 7 Extracting limit lines 15 8 General case using mixed-mode matrices 16 8.1 General 16 8.2 M-parameters 16 9 Submatrix DD 17 9.1 General 17 9.2 Equations to extract the cabling limit lines 17 9.2.1 General 17 9.2.2 Operational attenuation 17 9.2.3 Near-end crosstalk 17 9.2.4 Attenuation to far-end crosstalk ratio 17 9.2.5 Reflection (RL) 18 10.1 General 19 10.2.1 General 19 10.2.2 Wave attenuation 19				11	
5.6 S-matrix 13 5.7 Passivity 13 5.8 Operational reflection loss matrix 14 5.9 Transmission matrix (T-matrix) 14 5.10 S-matrix of cabling 14 6 Calculation with matrices using limit lines 15 7 Extracting limit lines 15 8 General case using mixed-mode matrices 16 8.1 General 16 8.2 M-parameters 16 8.2 M-parameters 16 9 Submatrix DD 17 9.1 General 17 9.2 Operational attenuation 17 9.2.1 General attenuation 17 9.2.2 Operational attenuation 17 9.2.3 Near-end crosstalk 17 9.2.4 Attenuation to far-end crosstalk ratio 17 9.2.5 Reflection (RL) 18 10 Component values to be used as input to the model 18 10.1 General 19 10.2.2 Wave attenuation 1		5.4	Operational scattering matrix	12	
5.7 Passivity 13 5.8 Operational reflection loss matrix 14 5.9 Transmission matrix (T-matrix) 14 5.10 S-matrix of cabling 14 6 Calculation with matrices using limit lines 15 7 Extracting limit lines 15 8 General case using mixed-mode matrices 16 8.1 General 16 8.2 M-parameters 16 9 Submatrix DD 17 9.1 General 17 9.2 Equations to extract the cabling limit lines 17 9.2.1 General 17 9.2.2 Operational attenuation 17 9.2.3 Near-end crosstalk 17 9.2.4 Attenuation to far-end crosstalk ratio 17 9.2.5 Reflection (RL) 18 10.1 General 19 10.2.1 General 19 10.2.2 Wave attenuation 19 10.2.3 Near-end crosstalk 19 10.2.4 Far-end crosstalk 19		5.5	General naming convention	12	
5.8 Operational reflection loss matrix 14 5.9 Transmission matrix (T-matrix) 14 5.10 S-matrix of cabling 14 6 Calculation with matrices using limit lines 15 7 Extracting limit lines 15 8 General case using mixed-mode matrices 16 8.1 General 16 8.2 M-parameters 16 9 Submatrix DD 17 9.1 General 17 9.2 Equations to extract the cabling limit lines 17 9.2.1 General 17 9.2.2 Operational attenuation 17 9.2.3 Near-end crosstalk 17 9.2.4 Attenuation to far-end crosstalk ratio 17 9.2.5 Reflection (RL) 18 10.1 General 19 10.2 Cable 19 10.2.2 Wave attenuation 19 10.2.3 Near-end crosstalk 19 10.2.4 Far-end crosstalk 19 10.2.5 Reflection 20		5.6			
5.9 Transmission matrix (T-matrix) 14 5.10 S-matrix of cabling 14 6 Calculation with matrices using limit lines 15 7 Extracting limit lines 15 8 General case using mixed-mode matrices 16 8.1 General 16 8.2 M-parameters 16 9 Submatrix DD 17 9.1 General 17 9.2 Equations to extract the cabling limit lines 17 9.2.1 General 17 9.2.2 Operational attenuation 17 9.2.3 Near-end crosstalk 17 9.2.4 Attenuation to far-end crosstalk ratio 17 9.2.5 Reflection (RL) 18 10.1 General 19 10.2.1 General 19 10.2.2 Wave attenuation 19 10.2.3 Near-end crosstalk 19 10.2.4 Far-end crosstalk 19 10.2.5 Reflection 20 10.3 Connections 21		5.7	Passivity	13	
5.10 S-matrix of cabling 14 6 Calculation with matrices using limit lines 15 7 Extracting limit lines 15 8 General case using mixed-mode matrices 16 8.1 General 16 8.2 M-parameters 16 9 Submatrix DD 17 9.1 General 17 9.2 Equations to extract the cabling limit lines 17 9.2.1 General 17 9.2.2 Operational attenuation 17 9.2.3 Near-end crosstalk 17 9.2.4 Attenuation to far-end crosstalk ratio 17 9.2.5 Reflection (RL) 18 10 Component values to be used as input to the model 18 10.1 General 19 10.2.2 Wave attenuation 19 10.2.3 Near-end crosstalk 19 10.2.4 Far-end crosstalk 19 10.2.5 Reflection 20 10.3 Connections 21 10.3.1 General 21		5.8	Operational reflection loss matrix	14	
6 Calculation with matrices using limit lines 15 7 Extracting limit lines 15 8 General case using mixed-mode matrices 16 8.1 General 16 8.2 M-parameters 16 9 Submatrix DD 17 9.1 General 17 9.2 Equations to extract the cabling limit lines 17 9.2.1 General 17 9.2.2 Operational attenuation 17 9.2.3 Near-end crosstalk 17 9.2.4 Attenuation to far-end crosstalk ratio 17 9.2.5 Reflection (RL) 18 10 Component values to be used as input to the model 18 10.1 General 19 10.2.1 General 19 10.2.2 Wave attenuation 19 10.2.3 Near-end crosstalk 19 10.2.4 Far-end crosstalk 19 10.2.5 Reflection 20 10.3 Connections 21 10.3.1 General 21		5.9			
7 Extracting limit lines 15 8 General case using mixed-mode matrices 16 8.1 General 16 8.2 M-parameters 16 9 Submatrix DD 17 9.1 General 17 9.2 Equations to extract the cabling limit lines 17 9.2.1 General 17 9.2.2 Operational attenuation 17 9.2.3 Near-end crosstalk 17 9.2.4 Attenuation to far-end crosstalk ratio 17 9.2.5 Reflection (RL) 18 10 Component values to be used as input to the model 18 10.2 Cable 19 10.2.1 General 19 10.2.2 Wave attenuation 19 10.2.3 Near-end crosstalk 19 10.2.4 Far-end crosstalk 19 10.2.5 Reflection 20 10.3 Connections 21 10.3.1 General 21 10.3.2 As a point source of disturbance 21		5.10	S-matrix of cabling	14	
8 General case using mixed-mode matrices. 16 8.1 General. 16 8.2 M-parameters 16 9 Submatrix DD. 17 9.1 General. 17 9.2 Equations to extract the cabling limit lines 17 9.2.1 General 17 9.2.2 Operational attenuation 17 9.2.3 Near-end crosstalk 17 9.2.4 Attenuation to far-end crosstalk ratio 17 9.2.5 Reflection (RL) 18 10 Component values to be used as input to the model 18 10.2 Cable 19 10.2.1 General 19 10.2.2 Wave attenuation 19 10.2.4 Far-end crosstalk 19 10.2.2 Wave attenuation 19 10.2.4 Far-end crosstalk 19 10.2.5 Reflection 20 10.3 Connections 21 10.3.1 General 21 10.3.2 As a point source of disturbance 21 <tr< td=""><td>6</td><td></td><td></td><td></td></tr<>	6				
8.1 General. 16 8.2 M-parameters 16 9 Submatrix DD 17 9.1 General. 17 9.2 Equations to extract the cabling limit lines 17 9.2 Equations to extract the cabling limit lines 17 9.2.1 General 17 9.2.2 Operational attenuation 17 9.2.3 Near-end crosstalk 17 9.2.4 Attenuation to far-end crosstalk ratio 17 9.2.5 Reflection (RL) 18 10 Component values to be used as input to the model 18 10.2 Cable 19 10.2.1 General 19 10.2.2 Wave attenuation 19 10.2.3 Near-end crosstalk 19 10.2.4 Far-end crosstalk 19 10.2.5 Reflection 20 10.3 Connections 21 10.3.1 General 21 10.3.2 As a point source of disturbance 21 10.3.3 As a transmission line 21	7	Extra	cting limit lines	15	
8.1 General. 16 8.2 M-parameters 16 9 Submatrix DD 17 9.1 General. 17 9.2 Equations to extract the cabling limit lines 17 9.2 Equations to extract the cabling limit lines 17 9.2.1 General 17 9.2.2 Operational attenuation 17 9.2.3 Near-end crosstalk 17 9.2.4 Attenuation to far-end crosstalk ratio 17 9.2.5 Reflection (RL) 18 10 Component values to be used as input to the model 18 10.2 Cable 19 10.2.1 General 19 10.2.2 Wave attenuation 19 10.2.3 Near-end crosstalk 19 10.2.4 Far-end crosstalk 19 10.2.5 Reflection 20 10.3 Connections 21 10.3.1 General 21 10.3.2 As a point source of disturbance 21 10.3.3 As a transmission line 21	8	Gene	ral case using mixed-mode matrices	16	
8.2 M-parameters 16 9 Submatrix DD 17 9.1 General 17 9.2 Equations to extract the cabling limit lines 17 9.2.1 General 17 9.2.2 Operational attenuation 17 9.2.3 Near-end crosstalk 17 9.2.4 Attenuation to far-end crosstalk ratio 17 9.2.5 Reflection (RL) 18 10 Component values to be used as input to the model 18 10.2 Cable 19 10.2.1 General 19 10.2.2 Wave attenuation 19 10.2.2 Wave attenuation 19 10.2.1 General 19 10.2.2 Wave attenuation 19 10.2.3 Near-end crosstalk 19 10.2.4 Far-end crosstalk 19 10.2.5 Reflection 20 10.3 Connections 21 10.3.1 General 21 10.3.2 As a point source of disturbance 21 10.3.					
9 Submatrix DD 17 9.1 General 17 9.2 Equations to extract the cabling limit lines 17 9.2.1 General 17 9.2.2 Operational attenuation 17 9.2.3 Near-end crosstalk 17 9.2.4 Attenuation to far-end crosstalk ratio 17 9.2.5 Reflection (RL) 18 10 Component values to be used as input to the model 18 10.1 General 19 10.2.1 General 19 10.2.2 Wave attenuation 19 10.2.3 Near-end crosstalk 19 10.2.4 Far-end crosstalk 19 10.2.5 Reflection 20 10.3 Connections 21 10.3.1 General 21 10.3.2 As a point source of disturbance 21		8.2			
9.2 Equations to extract the cabling limit lines 17 9.2.1 General 17 9.2.2 Operational attenuation 17 9.2.3 Near-end crosstalk 17 9.2.4 Attenuation to far-end crosstalk ratio 17 9.2.5 Reflection (RL) 18 10 Component values to be used as input to the model 18 10.1 General 19 10.2.1 General 19 10.2.2 Wave attenuation 19 10.2.3 Near-end crosstalk 19 10.2.4 Far-end crosstalk 19 10.2.5 Reflection 20 10.3 Connections 21 10.3.1 General 21 10.3.2 As a point source of disturbance 21	9	Subn			
9.2 Equations to extract the cabling limit lines 17 9.2.1 General 17 9.2.2 Operational attenuation 17 9.2.3 Near-end crosstalk 17 9.2.4 Attenuation to far-end crosstalk ratio 17 9.2.5 Reflection (RL) 18 10 Component values to be used as input to the model 18 10.1 General 19 10.2.1 General 19 10.2.2 Wave attenuation 19 10.2.3 Near-end crosstalk 19 10.2.4 Far-end crosstalk 19 10.2.5 Reflection 20 10.3 Connections 21 10.3.1 General 21 10.3.2 As a point source of disturbance 21		9.1	General	17	
9.2.1 General 17 9.2.2 Operational attenuation 17 9.2.3 Near-end crosstalk 17 9.2.4 Attenuation to far-end crosstalk ratio 17 9.2.5 Reflection (RL) 18 10 Component values to be used as input to the model 18 10.1 General 18 10.2 Cable 19 10.2.1 General 19 10.2.2 Wave attenuation 19 10.2.3 Near-end crosstalk 19 10.2.4 Far-end crosstalk 19 10.2.5 Reflection 20 10.3 Connections 21 10.3.1 General 21 10.3.2 As a point source of disturbance 21		••••			
9.2.2 Operational attenuation 17 9.2.3 Near-end crosstalk 17 9.2.4 Attenuation to far-end crosstalk ratio 17 9.2.5 Reflection (RL) 18 10 Component values to be used as input to the model 18 10.1 General 19 10.2 Cable 19 10.2.1 General 19 10.2.2 Wave attenuation 19 10.2.3 Near-end crosstalk 19 10.2.4 Far-end crosstalk 19 10.2.5 Reflection 20 10.3 Connections 21 10.3.1 General 21 10.3.2 As a point source of disturbance 21		9.2.1	· · · · · · · · · · · · · · · · · · ·		
9.2.3 Near-end crosstalk 17 9.2.4 Attenuation to far-end crosstalk ratio 17 9.2.5 Reflection (RL) 18 10 Component values to be used as input to the model 18 10.1 General 18 10.2 Cable 19 10.2.1 General 19 10.2.2 Wave attenuation 19 10.2.3 Near-end crosstalk 19 10.2.4 Far-end crosstalk 19 10.2.5 Reflection 20 10.3 Connections 21 10.3.1 General 21 10.3.2 As a point source of disturbance 21 10.3.3 As a transmission line 21		-			
9.2.4Attenuation to far-end crosstalk ratio179.2.5Reflection (RL)1810Component values to be used as input to the model1810.1General1810.2Cable1910.2.1General1910.2.2Wave attenuation1910.2.3Near-end crosstalk1910.2.4Far-end crosstalk1910.2.5Reflection2010.3Connections2110.3.1General2110.3.2As a point source of disturbance2110.3.3As a transmission line21					
9.2.5 Reflection (RL) 18 10 Component values to be used as input to the model 18 10.1 General 18 10.2 Cable 19 10.2.1 General 19 10.2.2 Wave attenuation 19 10.2.3 Near-end crosstalk 19 10.2.4 Far-end crosstalk 19 10.2.5 Reflection 20 10.3 Connections 21 10.3.1 General 21 10.3.2 As a point source of disturbance 21 10.3.3 As a transmission line 21					
10 Component values to be used as input to the model 18 10.1 General 18 10.2 Cable 19 10.2.1 General 19 10.2.2 Wave attenuation 19 10.2.3 Near-end crosstalk 19 10.2.4 Far-end crosstalk 19 10.2.5 Reflection 20 10.3 Connections 21 10.3.1 General 21 10.3.2 As a point source of disturbance 21 10.3.3 As a transmission line 21		9.2.5			
10.1 General 18 10.2 Cable 19 10.2.1 General 19 10.2.2 Wave attenuation 19 10.2.3 Near-end crosstalk 19 10.2.4 Far-end crosstalk 19 10.2.5 Reflection 20 10.3 Connections 21 10.3.1 General 21 10.3.2 As a point source of disturbance 21 10.3.3 As a transmission line 21	10	Com			
10.2 Cable 19 10.2.1 General 19 10.2.2 Wave attenuation 19 10.2.3 Near-end crosstalk 19 10.2.4 Far-end crosstalk 19 10.2.5 Reflection 20 10.3 Connections 21 10.3.1 General 21 10.3.2 As a point source of disturbance 21 10.3.3 As a transmission line 21					
10.2.1 General 19 10.2.2 Wave attenuation 19 10.2.3 Near-end crosstalk 19 10.2.4 Far-end crosstalk 19 10.2.5 Reflection 20 10.3 Connections 21 10.3.1 General 21 10.3.2 As a point source of disturbance 21 10.3.3 As a transmission line 21					
10.2.2 Wave attenuation 19 10.2.3 Near-end crosstalk 19 10.2.4 Far-end crosstalk 19 10.2.5 Reflection 20 10.3 Connections 21 10.3.1 General 21 10.3.2 As a point source of disturbance 21 10.3.3 As a transmission line 21		-			
10.2.3 Near-end crosstalk 19 10.2.4 Far-end crosstalk 19 10.2.5 Reflection 20 10.3 Connections 21 10.3.1 General 21 10.3.2 As a point source of disturbance 21 10.3.3 As a transmission line 21		-			
10.2.4 Far-end crosstalk .19 10.2.5 Reflection .20 10.3 Connections .21 10.3.1 General .21 10.3.2 As a point source of disturbance .21 10.3.3 As a transmission line .21		-			
10.2.5 Reflection 20 10.3 Connections 21 10.3.1 General 21 10.3.2 As a point source of disturbance 21 10.3.3 As a transmission line 21					
10.3 Connections 21 10.3.1 General 21 10.3.2 As a point source of disturbance 21 10.3.3 As a transmission line 21					
10.3.1General					
10.3.2As a point source of disturbance					
10.3.3 As a transmission line21					
	11				

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11.1 General	22 22
11.3 Submatrix DC 2 11.4 Submatrix CC 2	22
11.4 Submatrix CC2	
	22
Annex A (informative) Matrix conversion formulas2	23
A.1 Overview	23
A.2 Formulas	23
A.2.1 Mixed-mode to T-matrix	23
A.2.2 T-matrix to M-matrix	
A.2.3 Conversion matrices	23
Annex B (normative) Channel and permanent link models for balanced cabling2	25
B.1 General	
B.2 Insertion loss	
B.2.1 Insertion loss of the channel configuration	
B.2.2 Insertion loss of the permanent link configurations	
B.2.3 Assumptions for insertion loss	
B.3 NEXT	
B.3.1 NEXT of the channel configuration	
B.3.2 NEXT of the permanent link configurations	
B.3.2 NEXT of the permanent link configurations	
B.3.5 Assumptions for NEXT	
B.5.1 Return loss of the channel and permanent link configurations	
B.5.2 Assumptions for the return loss circuit analysis method	
B.6 PS ANEXT link modelling	
B.6.1 General	
B.6.2 PS ANEXT between connectors	
B.6.3 PS ANEXT between cable segments	
B.6.4 Principles of link modelling	
B.7 PS AACR-F link modelling	
B.7.1 General	
B.7.2 PS AFEXT between connectors	
B.7.3 PS AACR-F between cable segments	
B.7.4 Principles of link modelling	37
B.7.5 Impact of PS AACR-F in channels and links with substantially different	00
Iengths 3 B.8 Component assumptions for modelling purposes	
Annex C (informative) Terms and definitions4	
C.1 Comparison of namings	
C.2 General	
C.3 Background of terms and definitions	
C.3.1 Operational attenuation	
C.3.2 Operational transfer function (<i>T</i> _B)4	16
C.3.3 Image or wave transfer function (T)4	
C.3.4 Insertion transfer function of a two-port (<i>T</i> _{BI})4	16

Step of The State

C.3.5	Insertion transfer function $(T_{\sf BI})$ measured with a vector network	
	analyser	
C.3.6	Operational reflection loss transfer function ($T_{ref} = S_{ref}$) of a junction	
Bibliography		48
Figure 1 – Lin	k configurations of ISO/IEC 11801-1	7
Figure 2 – Ma	trix definition of a 4-port two twisted pair system	12
Figure 3 – Op	erational scattering parameters example from port 2	12
	insmission matrix concatenation showing an example of a 2-connector	
permanent lin	k	14
	aphical example of a NEXT calculation showing statistical results (red) ulation (blue)	16
	e pair M-matrix showing the submatrices	
	0 m cable return loss without reflection at both ends	
0) m cable return loss with a reflection of 0,03 at both ends (6 Ω	
	3 dB return loss at 1 MHz)	21
Figure A.1 – X	۲ matrices۲ matrices ۲۰۰۰	24
Figure B.1 – E	Example of computation of NEXT with higher precision	28
Figure B.2 – E	Example of increased impact of PS AFEXT	38
	Defining the operational attenuation and the operational transfer functions	45
-		
Figure C.2 – I	Defining the reflection transfer functions and the return loss of a junction	47
Table 1 – All 1	our ports operational scattering parameter definition	12
Table 2 – Equ	al S-parameters for real components	13
Table B.1 – Ir	isertion loss deviation	26
Table B.2 – M	odelling assumptions for cable transmission parameters	41

- 4 -

Table B.3 -	- Model input assur	nptions used in the	statistical	calculation	(Class I	Ξ _Α)	.41
Table B.4 -	- Model input assur	nptions used in the	statistical	calculation	(Class I	= _A)	.42

Table C.1 – Comparison of naming in ISO/IEC 11801-1 and ISO/IEC TS 11801-9903......43

INFORMATION TECHNOLOGY – GENERIC CABLING SYSTEMS FOR CUSTOMER PREMISES –

Part 9903: Matrix modelling of channels and links

FOREWORD

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ISO/IEC TS 11801-9903 has been prepared by subcommittee 25: Interconnection of information technology equipment, of ISO/IEC joint technical committee 1: Information technology. It is a Technical Specification.

This first edition of ISO/IEC TS 11801-9903 cancels and replaces ISO/IEC TR 11801-9903 published in 2015. This edition constitutes a technical revision.

This edition includes the following significant technical changes with respect to the previous edition:

- a) the addition of further clarifications of the relations of parameters described in this edition and referenced analogous parameters in IEC TR 62152, e.g. operational attenuation versus operational transfer loss;
- b) the introduction and description of the higher order M-parameters 8 × 8 matrix of mixedmode parameters, which includes the 4 × 4 submatrix of 4-port differential-mode-todifferential-mode (DD) parameters, among three other submatrices of mixed-mode parameters;
- c) Annex A, matrix conversion formulas, covers up to 16-port parameters matrices;
- d) the expanded Annex B description of example calculations for channel and permanent link, and updated component parameter tables.

The list of all currently available parts of the ISO/IEC 11801 series, under the general title *Information technology – Generic cabling for customer premises*, can be found on the IEC and ISO web sites.

The text of this Technical Specification is based on the following documents:

Draft	Report on voting
JTC1-SC25/2959/DTS	JTC1-SC25/2993/RVDTS

Full information on the voting for its approval can be found in the report on voting indicated in the above table.

The language used for the development of this Technical Specification is English.

This document was drafted in accordance with ISO/IEC Directives, Part 2, and developed in accordance with ISO/IEC Directives, Part 1, available at www.iec.ch/members experts/refdocs.

IMPORTANT – The 'colour inside' logo on the cover page of this publication indicates that it contains colours which are considered to be useful for the correct understanding of its contents. Users should therefore print this document using a colour printer.

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INTRODUCTION

The pass/fail limits for defined channel and permanent link cabling configurations have an implicit impact on the component limits for the cabling components used. The channel configurations and the link configurations are specified in ISO/IEC 11801-1:2017, Clause 6 and Clause 7, respectively.

The permanent link configurations, which represent the fixed portion of the cabling, have two possible topologies:

- a connection plus a segment of cable plus a connection (2-connector topology);
- a connection plus a segment of cable plus a connection plus another segment of cable plus another connection (3-connector topology).

The link configurations of ISO/IEC 11801-1 are shown in Figure 1.

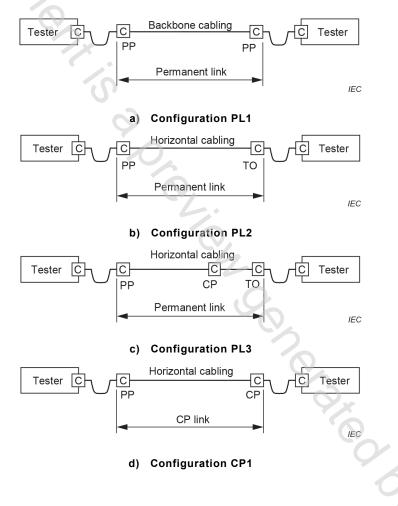


Figure 1 – Link configurations of ISO/IEC 11801-1

This document includes models and assumptions, which support pass/fail limits for the channel and permanent link test configurations in ISO/IEC 11801-1. These are based on the performance requirements of cable and connecting hardware as specified in IEC standards.

This document provides reasonable assurance that a channel created by adding compliant patch cords to a previously certified permanent link will meet the applicable channel performance limits.

Over the years the frequencies of the classes increased, but the theory for calculating the limits stayed the same. Especially the higher order effects had to be considered and in the end only by doing a Monte Carlo calculation, assuming that not all components would be at the limit at the same time, allowed compliance to be proved.

The model uses two pairs for all calculations. The limits are equal for pairs or pair combinations but in reality measured values could be different. If results are required that need more pairs to be considered, then this calculation can be done based on the results from multiple two-pair calculations with appropriate inputs (worst case). An example of such a calculation is the power sum and average limit lines for four pairs.

Symmetry and additional contributions that result from unbalanced signals and differential-tocommon and common-to-differential mode coupling are included in this document by increasing the matrix size.

For details on the naming of transmission parameters, see Clause 3 and Clause C.1.

INFORMATION TECHNOLOGY – GENERIC CABLING SYSTEMS FOR CUSTOMER PREMISES –

Part 9903: Matrix modelling of channels and links

1 Scope

This part of ISO/IEC 11801, which is a Technical Specification, establishes a matrix-model for formulating limits for mixed-mode parameters within and between two pairs of balanced cabling. This is for the purpose of supporting new, improved balanced cabling channel and link specifications.

2 Normative references

The following documents, in whole or in part, are normatively referenced in this document and are indispensable for its application. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

ISO/IEC 11801-1, Information technology – Generic cabling for customer premises – Part 1: General requirements

3 Terms, definitions and abbreviated terms

3.1 Terms and definitions

For the purposes of this document, the terms and definitions given in ISO/IEC 11801-1 and the following apply.

ISO and IEC maintain terminological databases for use in standardization at the following addresses:

- IEC Electropedia: available at http://www.electropedia.org/
- ISO Online browsing platform: available at http://www.iso.org/obp

3.1.1 attenuation diminishing of signal strength

Note 1 to entry: Details need to be added to indicate the exact usage.

3.1.2 connection two mated connectors

EXAMPLE Jack and plug.

3.1.3 image attenuation wave attenuation

attenuation when a two-port is terminated by its input and output characteristic impedances with no reflections at input and output

62

Note 1 to entry: The wave attenuation of cables is length scalable.